WHAT IS CLAIMED IS:

1. An information handling system, comprising:

a communications medium connecting a plurality of electronic devices;

an operating system, including device drivers, capable of configuring communications between one or more applications and the communications medium;

a detector coupled to the communications medium, the detector receiving error signals transmitted by the communications medium, one or more error signals associated with one of the electronic devices; and

a BIOS coupled to the detector, the BIOS capable of determining an electronic device associated with a first error signal and the BIOS generating a hot-eject signal identifying that electronic device in response to the first error signal;

wherein the operating system blocks communications between the applications and the identified electronic device in response to the BIOS generating the hot-eject signal.

2. The information handling system of claim 1, wherein the first error signal is a PCI parity error (PERR#) or a PCI system error (SERR#).

3. The information handling system of claim 1, wherein the BIOS creates a log entry in response to receiving the first error signal.

4. The information handling system of claim 1, wherein the BIOS does not generate a hot-eject signal in response to a second error signal.

5. The information handling system of claim 1, wherein the identified electronic device is an expansion card and further comprising an expansion connector coupled to the communications medium and the expansion card, wherein the expansion connector ceases communications with the communications medium after the first error signal is transmitted as a result of the first error signal.

6. The information handling system of claim 5, wherein embedded server management causes the expansion connector to cease communications with the communications medium in response to the first error signal.

7. The information handling system of claim 1, wherein the communications medium is a PCI local bus.

8. The information handling system of claim 1, wherein the identified electronic device is an on board device and the on board device is programmed to cease communications with the communications medium after the first error signal is transmitted as a result of the first error signal.

9. The information handling system of claim 1, wherein the hot-eject signal is a system control interrupt.

10. A method of notifying an operating system of a communications error, comprising:

detecting a first error signal on a communications medium, the communications medium connecting a plurality of electronic devices;

determining an electronic device associated with the first error signal;

generating a hot-eject signal that identifies the electronic device associated with the first error signal; and

blocking communications between at least one application and the identified electronic device with an operating system that includes device drivers in response to receiving the hot-eject signal.

11. The method of claim 10, wherein the first error signal is a PCI parity error (PERR#) or a PCI system error (SERR#).

12. The method of claim 10, further comprising the step of creating a log entry in response to the first error signal.

13. The method of claim 10, wherein a hot-eject signal is not generated in response to a second error signal.

14. The method of claim 10, wherein the identified electronic device is an expansion card coupled to an expansion connector that is also coupled to the communications medium and further comprising the step of disabling communications between the communications medium and the expansion connector after the first error signal is detected in response to the first error signal.

15. The method of claim 14, wherein the step of disabling is performed by embedded server management.

16. The method of claim 10, wherein the communications medium is a PCI local bus.

17. The method of claim 10, wherein the identified electronic device is an on board device and further comprising the step of programming the on board device to cease communications with the communications medium after the first error signal is detected in response to the first error signal.

18. The method of claim 10, wherein the hot-eject signal is a system control interrupt.

19. An information handling system, comprising:

a bus coupling a plurality of electronic devices;

an operating system, including device drivers, capable of configuring communications between one or more applications and the plurality of electronic devices coupled to the bus;

a detector coupled to the bus, the detector receiving error signals transmitted by the bus, one or more error signals associated with one of the electronic devices; and

a BIOS coupled to the detector, the BIOS capable of determining an electronic device associated with a first error signal, the first error signal being a parity or system error signal, and the BIOS generating a hot-eject signal system control interrupt identifying that electronic device in response to the first error signal;

wherein the operating system blocks communications between the applications and the identified electronic device in response to the BIOS generating the hot-eject signal system control interrupt.

20. The information handling system of claim 19, wherein the operating system continues operating normally after receiving the hot-eject signal system control interrupt and blocking communications between the applications and the identified electronic device.